

50,

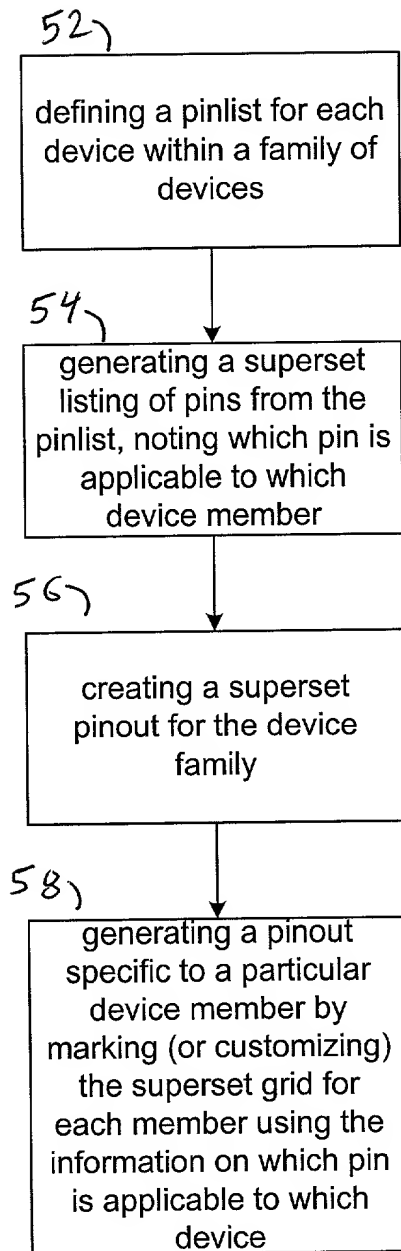


FIG. 1a

50)

52)

defining a pinlist for each
device within a family of
devices

54')

generating a superset
listing of pins from the
pinlist

56)

creating a superset
pinout for the device
family

58')

generating a pinout
specific to a particular
device member by
deciding and then
marking (or customizing)
the superset grid for
each member using the
pinlist

FIG. 1b

FIG. 3

FIG. 3